



Department of Electronics and Communication Engineering  
Khulna University of Engineering & Technology  
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**Nazifa Tabassum**

Assistant Professor

**Research Area** Digital Signal Processing,  
Biomedical Signal Processing, Biomedical  
Image Processing, VLSI Design, System  
design on FPGA

## Education

### Master of Science in Electronics and Communication Engineering

Khulna University of Engineering & Technology, Bangladesh (2020) Student Type: Regular,  
Department of ECE

### Bachelor of Science in Electronics and Communication Engineering

Khulna University of Engineering & Technology, Bangladesh (2017) Group: ECE, Student Type: Regular, Merit Position: 2nd,

### Higher Secondary Certificate (HSC)

Govt. P. C. College, Bagerhat, Bangladesh (2012) Group: Science, Student Type: Regular, Achievement: Board Scholarship

### Secondary School Certificate (SSC)

Bagerhat Govt. Girls High School, Bagerhat, Bangladesh (2010) Group: Science, Student Type: Regular, Achievement: Board Scholarship

### Junior Scholarship

Bagerhat Govt. Girls High School, Bagerhat, Bangladesh (2007) Achievement: Talentpool Scholarship

## Service Records

- **Assistant Professor**  
**Department/Section:** Electronics and Communication Engineering  
**Khulna University of Engineering & Technology** From 01-01-1970 to 01-01-1970
- **Lecturer**  
**Department/Section:** Electronics and Communication Engineering  
**Khulna University of Engineering & Technology** From 01-01-1970 to 01-01-1970

## Research Interest

Digital Signal Processing, Biomedical Signal Processing, Biomedical Image Processing, VLSI Design, System design on FPGA

## Publication

### Books

### Journals

2. Hassan, M., Islam, S. M. R. and Tabassum, N. (March 31, 2018), "Design and Implementation of Sampling Rate Conversion System for Electroencephalogram (EEG) on FPGA Device," **International Journal of Electronics and Communication Engineering, Impact Factor (JCC): 4.9564**, vol7, no.2, pp.9-22

1. Tabassum, N., Islam, S. M. R. and Huang, X. (April 2018), "Bio-chip Design Using Multi-rate System for EEG Signal on FPGA," **International Journal of Image, Graphics and Signal Processing (IJIGSP)**, MECS Pres, vol10, no.4, pp.39-47

### Conference

3. Tamanna, R., Islam, S. M. R. and Tabassum, N., "Design and Implementation of DWT for EEG Signal on FPGA," **4th International Conference on Electrical Engineering and Information & Communication Technology (iCEEICT 2018)**

2. Tabassum, N., Islam, S. M. R. and Huang, X. (27-29 December 2017), "Novel Multirate Digital Filter for EEG on FPGA," **International Conference on Electrical & Electronic Engineering (ICEEE 2017)**

1. Tabassum, N., Islam, S. M. R. and Huang, X. (7-9 December 2017), "Implementation of Biochip on Multirate System for EEG signal on ALTERA Cyclone Device," **3rd International Conference on Electrical Information and Communication Technology (EICT)**